

CHIP MODULE

CROSS-REFERENCE TO RELATED APPLICATIONS


This application is a continuation of International Patent Application Serial No. PCT/DE02/02758, filed July 26, 2002, which published in German on February 27, 2003 as WO 03/017195 A1 and which is incorporated herein by reference in its entirety.

FIELD OF THE INVENTION

The present invention relates to a chip module which is provided for use with smart cards and has a further component in addition to a chip.

BACKGROUND OF THE INVENTION

In the future, smart cards are intended to be equipped with enhanced functions. These include not only the transmission of data by means of electrical data lines but also communication of information by electromagnetic beams which, in the simplest case, are used for optical reproduction of information. Therefore, in addition to the chip, a further component is intended to be integrated in a smart card, said further component being provided for taking up, emitting, reflecting or partially shielding electromagnetic radiation. What may be involved in this case is a display part (display) which itself emits electromagnetic waves in the optical range or, as in the case of an LCD, reflects and/or partially shields light, in order thus to effect an optical display. Moreover, said component may be a sensor or detector for radiation, which may be provided for taking up radiated information which is transmitted, e.g., by means of a suitably modulated electromagnetic wave. The component may equally

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component is electrically connected to the semiconductor chip. What is essential to the chip module according to the invention is that the substrate is transmissive to the relevant radiation to a sufficient extent and at least in a region occupied by the further component.

What is thus achieved is that the chip module may be fitted at a top side of a card body provided with corresponding cutouts such that the main side of the substrate opposite to the components forms a top side of the smart card formed thereby. The chip and the further component are accordingly situated in the cutouts of the card body, so that the smart card can be produced with a planar top side.

The chip module therefore affords a connected arrangement of the essential constituent parts which can be produced in a simple manner. The chip and the further component connected thereto are jointly fixed to a radiation-transmissive substrate and can be inserted altogether into a card body. The substrate may be a film which is transmissive to electromagnetic radiation in the envisaged waveband, in preferred refinements a light-transmissive film, which is therefore suitable for the provision of an optical display device. A polarization film that is used any way for the top side of the LCD display may be involved in this case; it may be, in particular, a film made of polyethylene terephthalate (PET). In principle, the radiation transmissivity need only be present in the region of the component fitted. Other regions of the substrate may be covered or treated in some way such that the radiation transmissivity is reduced or no longer ensured there.

BRIEF DESCRIPTION OF THE DRAWINGS

Examples of the chip module are described in more detail below with reference to the accompanying figures 1 and 2.

Figure 1 shows a detail from a typical exemplary embodiment of the chip module in cross section.

Figure 2 shows the arrangement in accordance with figure 1 from the viewing direction from below.

DETAILED DESCRIPTION OF THE PREFERRED MODE OF THE INVENTION

Figure 1 illustrates an arrangement in which a further component 2, electrically conductive connections 3, a semiconductor chip 4, adhesive layers 5, a plated-through-hole 6, a contact area 7, and a driver chip 20 for the further component are present on a substrate 1. In the case of the chip module according to the invention, the substrate 1, the chip 4, the further component 2 and the electrically conductive connection 3 are present independently of the possible individual embodiments, these components being provided on the same main side of the substrate 1 of planar extent.

The electrically conductive connections 3 are preferably conductor tracks made of an electrically conductive material which are applied on the relevant main side of the substrate 1 and are provided for electrical connection between the chip 4 and the further component 2. The conductor tracks may additionally be patterned in such a way that at least a portion of the conductor tracks functions as an antenna which is provided for a contactless transmission of data and/or energy. A conductor spiral or at

In principle, it is possible to connect connecting contacts of the chip 4 and connecting contacts of the further component 2 to contact areas - provided therefor - of the electrically conductive connections 3 by means of connecting wires (bonding wires). In the preferred exemplary embodiment illustrated in figure 1, the components are applied on the electrically conductive connections 3 in the manner of flip-chip mounting (FCOS, flip-chip on substrate) such that the connecting contacts of the chip 4 and of the further component 2 are electrically conductively connected to the associated conductor tracks.

If the further component 2 is an optical display, in accordance with a preferred exemplary embodiment, said component is preferably provided with a driver 20

an electrically conductive connection through the substrate vertically with respect to the area of the substrate 1 and connects the electrically conductive contact area 7 to a connecting contact of the chip 4 which is provided therefor or to a conductor track or electrically conductive connection 3 which is provided therefor.

The arrangement of the components of the chip module enables the entire arrangement to be inserted into an otherwise homogeneous card body, in that e.g., those portions of the substrate 1 which project beyond the chip 4 and the further component 2 along the edges of the substrate are adhesively bonded onto an outer region of a top side of a card body, said outer region surrounding a cutout. The components fitted on the underside of the substrate are embedded in a filling composition or an adhesive, if appropriate, in the cutout of the card body.

In further-reaching exemplary embodiments, it is provided that the substrate 1 may comprise a plurality of layer elements or be composed of materials that are different in regions or a material which is configured differently in regions. The substrate may comprise, e.g., both a radiation-transmissive polarization layer and a substrate layer provided specifically for the carrier function of the substrate. If said substrate layer is not transmissive to the radiation, it is necessary to provide a cutout in the substrate layer in the region 10, in which the further component is arranged.

As is indicated in figure 1, the further component 2 may be fixed to the substrate with an edge-side region of the top side facing the substrate 1. Instead of this, the further component 2 may also be fixed to the substrate 1 over the whole area if, for this purpose, a material transmissive to the envisaged radiation is used as an adhesive. The driver 20 may also be an integral constituent part of the further

component 2 or be arranged between the substrate 1 and the further component 2. In principle, the distance between the substrate 1 and the further component 2 may be arbitrarily small or else significantly larger than is reproduced in figure 1, in a manner not true to scale.

Figure 2 illustrates a plan view of the example of the chip module in accordance with figure 1 from a viewing direction from below. Thus, in this viewing direction, it is possible to discern the driver 20 on the underside of the further component 2. The electrically conductive connections 3 between the chip 4 and the further component 2 are represented here by five conductor tracks illustrated in a simplified manner. The size relationships between the chip 4, the further component 2 and the driver 20 are not defined. The chip 4 may be made larger or smaller than is illustrated in the figure. A sensor as further component 2 will possibly have considerably smaller dimensions than the chip 4. An optical display will possibly comprise a significant region of the total area of the substrate 1. No fundamental limits are imposed on these configurations within the scope of the invention. However, consideration is to be given to ensuring that the chip module has a robustness and fracture resistance necessary for easy producibility.